



(19) Europäisches Patentamt
European Patent Office
Office européen des brevets



(11) EP 0 921 690 A2

(12)

EUROPEAN PATENT APPLICATION

(43) Date of publication:
09.06.1999 Bulletin 1999/23

(51) Int Cl. 6: H04N 7/62

(21) Application number: 99200377.2

(22) Date of filing: 30.08.1995

(84) Designated Contracting States:

AT DE FR GB

Designated Extension States:

LT LV SI

(30) Priority: 07.09.1994 US 302144

(62) Document number(s) of the earlier application(s) in accordance with Art. 76 EPC:
95927940.7 / 0 727 126

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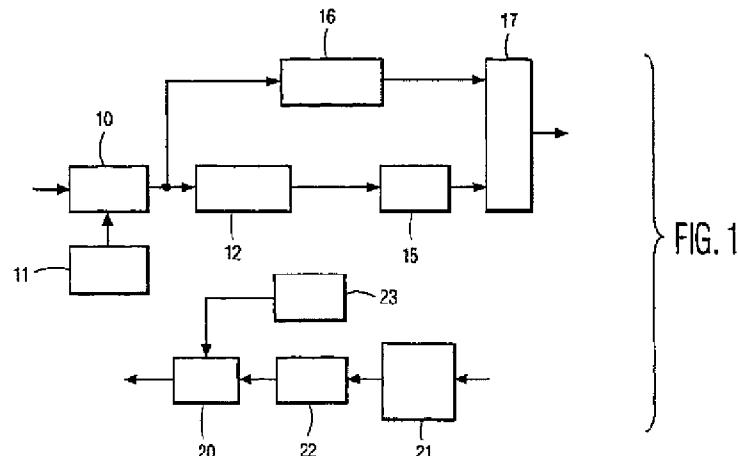
Remarks:

This application was filed on 09 - 02 - 1999 as a divisional application to the application mentioned under INID code 62.

(54) MPEG information signal conversion system

(57) A method of transmitting timing critical data via an asynchronous channel. The timing critical data can be an MPEG transport stream of packets. The asynchronous channel can be a computer or telephone network, a digital storage media such as a digital VCR, or a digital interface. The packets are processed serially through a remuxer to obtain a constant rate and delivered to and consumed by one or more target decoders, for example, inside a TV set or in a set-top decoder. To prevent overflow of the transport buffers inside these de-

coders, a single monitor-scheduler is provided which monitors the transport buffers and delivers to each the packets wanted scheduled so as to avoid buffer overflow and loss of information. The method also includes restamping the transport packets with new PCRs. The remuxing scheme is simple enough to implement on DVCR or other consumer applications. Also described is a method for recording the output stream which selects out desired program material and tags the transport packets with SOA tags.



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Description**BACKGROUND OF THE INVENTION**

5 [0001] The invention relates to a system for recording and playing back an MPEG information signal in tracks on a record carrier, and specifically a record carrier of the Digital Video Cassette Recorder (DVCR) type.

[0002] An MPEG information signal comprises a succession or stream of transport packets, which includes a data compressed digital video signal and a corresponding data compressed digital audio signal (and sometimes data signals), for broadcasting purposes or for transmission via a cable network. The MPEG information signal is in the form of transport packets having either an equal length or a variable length in time. In both cases, however, a transport packet comprises 188 bytes of information, the first byte of which is a synchronization byte.

10 [0003] A transmission such as an MPEG information signal in the form for recording on and reproduction from a record carrier, such as a magnetic record carrier as a tape, require special measures to be taken in order to realize such kind of transmission via the known tape format.

15 [0004] Storing a packet sequence number has its advantages if an MPEG data stream is received having a constant bit or transport rate without any gaps between packets, and comprising a number of different video programs interleaved in the MPEG data stream. Generally, such data stream may have too high a bit rate for recording the total data stream on the record carrier. For example, the MPEG bit rate for cable transmission is 45 Mbps, whereas the record carrier typically records with a 25 Mbps bit rate. The recording arrangement now comprises a program selector for retrieving 20 one or multiple programs from the MPEG data stream so as to obtain the MPEG information signal for recording. As information corresponding to only one program is included in a MPEG transport packet, such a program selector selects, which is per se known, only those transport packets from the MPEG data stream that comprise information corresponding to wanted program(s). That means that some packets of the original MPEG data stream received are deleted. Upon reproduction, however, a valid MPEG video signal in accordance with the MPEG standard, however now comprising 25 only the wanted programs, must be regenerated or recreated. By a "valid" MPEG signal or transport stream of transport packets of valid timing-critical-information is meant a stream that satisfies the following requirements:

30 1. The program clock reference (PCR) in the packet is OK. The PCR is, typically, a 33 bit value of a sample of the local clock in the transmitter encoder. The PCR is used for clock recovery so that in the decoder, the local clock can be sync'd to the encoder local clock.

2. Accumulated change to each PCR through the network must be kept within the limit specified by MPEG.

3. The decoder transport buffers do not overflow.

35 [0005] Such regenerated data stream should have the transport packets that were selected upon recording in the same order. Upon recording a sequence number can be added to each transport packet received, also for any packets that will be deleted. The sequence numbers of the packets that are selected and stored may be stored in the third block section of the signal blocks in which a transport packet is stored. Upon reproduction, a sequence of numbers is retrieved, where subsequent numbers will not necessarily be next higher numbers. In that situation one or more dummy packets must be inserted so as to regenerate the replica of the original MPEG data stream.

40 [0006] It will also be apparent that a reproducing arrangement will be needed which is adapted to each specific embodiment of the recording arrangement, so as to enable a reproduction of the MPEG information signal recorded on the record carrier.

45 [0007] The two related copending applications, whose full contents are incorporated herein by reference, describe such systems which solve a problem arising from the asynchronous nature of a channel represented by the DVCR and the necessity for preserving the timing critical data incorporated in the MPEG transport stream so that it can be reconstituted as a valid MPEG information signal upon playback for reproduction on a conventional TV set. The systems described involve tagging transport packets of the MPEG data stream, before inputting to the channel, with timing information, and using the timing information at the output end of the channel to recreate the proper data timing. Various schemes are described for packing the timing information tags with each or a plurality of transmission units of the transport stream. Using this basic tagging mechanism, transport streams of various types can be recorded and played back without losing any of the information in the original transmission. Where the transport rate of the transport stream is unknown, or with gaps between the transport packets (i.e., bursty), or the transport rate changes, then the referenced related applications describe ways of handling such data streams.

55 [0008] When, on the other hand, the transport rate of the incoming transport stream is constant and unknown, the related applications also describe schemes for handling this situation. Thus, using a combination of Time-Of-Arrival (TOA) and Sequence-Of-Arrival (SOA) tagging as described in the related applications, an MPEG-2 transport stream of unknown but constant transport rate can be recorded and recreated on playback. In this case, however, there must not be any gaps between the transport packets at the input.

BRIEF SUMMARY OF THE INVENTION

[0009] An object of the invention is a system for recording and playback of MPEG information using a DVCR.

5 [0010] A further object of the invention is a system for generating a fixed-rate, constant transport stream from an incoming unknown transport stream, which is possibly at varying rate, and/or bursty.

[0011] Another object of the invention is a system for generating a fixed-rate, constant valid MPEG transport stream from an incoming unknown MPEG transport stream, which is possibly at varying rate, and/or bursty.

[0012] Still another object of the invention is a remuxing scheme for MPEG transport streams which is simple enough to implement in DVCR or other consumer applications.

10 [0013] In accordance with one aspect of the invention, the packets are processed serially through a remuxer to obtain a constant rate and delivered to and consumed by one or more target decoders, for example, inside a TV set or in a set-top decoder. To prevent overflow of the transport buffers inside each of these decoders, a single monitor is provided which monitors all of the transport buffers and delivers to each the packets wanted timed in a manner that will avoid buffer overflow and loss of information. This aspect of the method of the invention requires that the transport packets be restamped with a new PCR. Looked at from another view, this aspect of the invention basically involves remuxing the transport stream to a known and fixed case, and then applying the solution described in the referenced related applications for the case of a transport stream with a constant and known transport rate.

15 [0014] The invention is not limited to application to an MPEG information signal and can also be applied to asynchronous channels other than a DVCR. In addition to transmitting MPEG data streams, there are various other applications that may require the transmission of timing critical data over an asynchronous channel. Asynchronous here means that the physical data rate of the channel is different from the transport rate, the rate of the data to be transmitted, so that the bitwise timing of data is not maintained through the channel transmission.

20 [0015] In the MPEG transport stream as an example of timing critical data, the relative arrival time of a datum which represents timing information of the transport stream, i.e., the PCR, must not be changed beyond a specified tolerance through transmission without changing the PCR value accordingly. This is because otherwise, the Phase Lock Loop (PLL) circuitry of a decoder will fail to regenerate the data clock, and the buffers may under/overflow. This problem of how to transmit timing critical data over an asynchronous channel without changing any datum to be transmitted also exists where the asynchronous channel is a computer network, a telephone network or a digital interface, e.g. P1394.

25 [0016] In accordance with another aspect of the invention, an improved remux method is described which does not have to perform complex de-multiplexing/re-multiplexing but relies instead on scheduling each packet without changing the order of the useful packets. This method offers the important advantage that the required hardware is much less expensive and thus easier to implement in low-cost consumer equipment.

30 [0017] The various features of novelty which characterize the invention are pointed out with particularity in the claims annexed to and forming a part of this disclosure. For a better understanding of the invention, its operating advantages and specific objects attained by its use, reference should be had to the accompanying drawings and descriptive matter in which there are illustrated and described the preferred embodiments of the invention, wherein like reference numerals depict the same or similar components.

BRIEF DESCRIPTION OF THE DRAWINGS

40 [0018] In the drawings:

Fig. 1 corresponds to Fig. 18 of the second related application and shows a system for handling a transport stream with a known and constant transport rate, from both the recording and playback standpoints;

45 Fig. 2 shows an example of the input and output data streams from the apparatus of Fig. 1;

Fig. 3 illustrates a remuxing and DVCR recording system;

Fig. 4 shows an example of the input and output data streams from the apparatus of Fig. 3;

Fig. 5 is a schematic block diagram of a packet processor feeding apparatus with a target decoder;

50 Fig. 6 is a block diagram of one form of remuxing system in accordance with the invention;

Fig. 7 is a block diagram combining the system of Fig. 1 and the remuxing system of Fig. 6 as one form of another embodiment of the invention;

Figs. 8A and 8B are graphs illustrating the effects of a remuxing scheme which does not monitor the transport buffer under two different conditions;

55 Figs. 9A and 9B are graphs illustrating the effects of a remuxing scheme which does monitor the transport buffer under the same two different conditions of Figs. 8A and 8B.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0019] In order to better understand the invention, we will first describe the system described and claimed in the two related applications for handling a transport stream with a constant, known transport rate.

[0020] Fig. 1 shows such a system applied to an MPEG application where R represents the transport rate of the MPEG data stream subdivided into transmission units in the form of a succession of transport packets from a digital interface (D-I/F). In the example given, the incoming transport rate is 45 Mbps, to be recorded on a DVCPR at 25 Mbps, and then recreated as a valid MPEG signal at 45 Mbps, for playback on a standard TV set.

[0021] The incoming data stream from D/I/F is received by a known tagging means 10 for tagging each incoming packet with a SOA tag requiring only a counter 11 incrementing at the arrival of each transport packet. The tagged packets then go to a selection means 12 for selecting the desired program material, and the selected packets stored temporarily in a local buffer 15. As explained in the referenced related applications, trickmode packets can be generated from the incoming transport stream at block 16 and intermixed with the desired program material by a MUX block 17 at the desired transport rate for recording on the DVCR. The tagging bits are recorded onto the DVCR tape along with the corresponding transport packets using the extra bits available from the 2 to 5 sync blocks mapping as described in the related cases.

[0022] On playback, each recorded packet is read out at its correct sequence according to the SOA stamp information under control of a read control block 20 via a local buffer 22. A DE-MUX block 21 acts to separate the packet stream, and the gaps formed by the non-selected program material packets in the output stream are filled in by supplying Null packets from a Null packet generator 23. During playback, each time a "discontinuity" in the SOA tag is detected, it is assumed to have come from a transport packet that has not been recorded. These "missing" packets are replaced with the Null packets. All transport packets are thus output at the known and constant transport rate.

[0023] Fig. 2 gives an example of the transport stream resulting. In the upper diagram, as an example, the input transport stream is a two program stream: programs A and B. We wish to record only program A, and hence strip out the B packets in the selection block 12. On playback, all the packets belonging to program A are reproduced at precisely their original times and rates, and the gaps are filled in with the Null packets (lower diagram). If the input stream was a valid MPEG signal, the output stream will also be valid.

[0024] As will be clear from the foregoing explanations, to maintain the interoperability between MPEG applications, it is necessary for the DVCR to generate a valid MPEG transport stream, preferably at a fixed-rate and constant (i.e., without any gaps between packets). Generating a fixed-rate constant transport stream for input to the apparatus of Fig. 1 from an incoming unknown transport stream, which is possibly at varying rate and/or bursty, meaning that there are gaps between the packets, is equivalent to generating a new transport stream by so-called remuxing. Remuxing by DVCR is comprised of selection of necessary packets, rescheduling of the timing of each packet, and multiplexing of the selected packets with Null packets to fill out the gaps in the transport stream. Whenever remuxing is done, the following remux requirements must be met:

- (a) Timing jitter of each PCR (incorporated in each transport packet) must be kept within an acceptable limit.
- (b) Accumulated change to each PCR through the network must be kept within the limit specified by MPEG.
- (c) Generated transport stream must not overflow the transport buffer of each elementary stream decoder.

[0025] One form of apparatus according to the invention is illustrated in Fig. 3, a combination of SOA and remuxing. In this case, after the remuxing 80, the packet stream at the new rate is tagged 81 and any Null packets removed in the local buffer block 82. However, in this case, the duration of the output packets is different. Fig. 4 shows in the upper diagram the input stream to the remuxer 80, and in the lower diagram the output stream to the D/I/F. The transport rate is now constant, but the packets need restamping with new PCRs because the old PCRs are no longer valid. Another problem appears, illustrated in Fig. 5.

[0026] Fig. 5 shows schematically a valid MPEG signal input to a packet processor 84, which may be the system of Figs. 1 and 3. It outputs a valid transport stream like that of Fig. 4 (lower diagram) to, for example, a selector 86 selecting packets for each elementary stream decoder 85 which includes a transport buffer 87, referred to herein as the "target decoder" and "target buffer". For the system to perform properly, the target buffers of each decoder provided must all be managed to avoid overflow. The problems are illustrated in Figs. 8A and 8B.

[0027] In these figures, X represents the input transport rate, Y represents the output transport rate, and R represents the read-out or emptying or leak rate of the transport buffer in the target decoder. Fig. 8A shows the effect of remuxing without monitoring of the transport buffer where $R < Y < X$. The row labelled Input is the sequence of input transport packets, and the row below labelled output is the sequence of output transport packets over a time t. The graph above indicates the fullness of the transport buffer over time t, with the dashed line 29 at top labelled Th representing a full buffer, and the dotted diagonal lines 30 representing the input stream. Where the solid line curve 31 representing the output stream crosses the threshold line 29, shown at 32, bits of certain output packets will be lost, which will occur

with packets 7, 10, etc. The case illustrated is where the input rate is faster than the output rate.

[0028] In Fig. 8B, the case where the input rate is slower than the output rate is given, represented by $R < X < Y$, with the dotted lines 34 again representing the input stream, and the solid line curve 35 again representing the output stream. Here, too, without monitoring, when the curve 35 crosses the threshold line 29, shown at 36 and 37, bits of 5 certain output packets will be lost, which will occur with packets 6, 9, etc.

[0029] A feature of the invention is a remuxing scheme for MPEG transport streams which is simple enough to implement on DVCR or other consumer applications.

[0030] This aspect of the present invention is based upon the following new concepts and understandings.

- 10 1. Incoming transport streams have small enough timing jitter; hence, requirement (a) can be met simply by re-stamping PCR's, using an appropriate local clock.
2. Incoming transport streams have enough head room for the PCR change; hence, the requirement (b) can be met by the remuxing scheme described below. By "head room" is meant the unused portion of the limit as defined in the MPEG standard.

[0031] General remuxing includes clock regeneration of each program, de-multiplexing of the input to separate each elementary stream, keeping track of each transport target buffer, calculation of a new schedule, re-multiplexing of the elementary streams, restamping of PCR, and so on. As will be recognized, such general remuxing requires complex 15 software and hardware not implementable in typical low-cost consumer appliances. A feature of our invention is based on a simpler remuxing scheme, which does not perform de-multiplexing/re-multiplexing but simply schedules each transport packet without changing the order of the useful packets. This approach requires certain basic assumptions:

- 20 1. The total net rate of the wanted program(s) in the input transport stream must be less than the output transport stream rate (the remux rate);
2. The input transport stream rate can, however, be unknown, bursty and/or at any rate, higher or lower;
3. The remux rate is known, fixed and constant.

[0032] In accordance with an aspect of the method of the present invention, the remuxing goes as follows, reference being made to Fig. 6 which is a block diagram of one form of remuxing apparatus in accordance with the invention:

- 30 1. The necessary packets are selected from the incoming transport stream by a filter or selector 40.
2. The packets containing the PCR are tagged 41 with the sampled value of a local clock 39.
3. Each packet is stored and kept in a local buffer 42 until it is read out to a packet store 44.
4. Whenever the packet store 44 is empty and there is at least one packet in the buffer 42, the first packet in the 35 buffer 42 is read out and moved to the packet store 44. Necessary information of the packet is sent to a scheduler 45 at the same time.
5. The scheduler 45 checks whether outputting of the packet in the packet store 44 will overflow the transport buffer 87 of the corresponding elementary stream decoder 85 (Fig. 5) and signals it to a MUX 47.
6. If the packet store 44 has a packet and the scheduler 45 signals that the decoder transport buffer will be OK, the MUX 47 selects and reads out the transport packet in the packet store 44. Otherwise, the MUX 47 selects and 40 outputs a Null packet from a Null packet generator 49. The packet in the packet store 44 remains there until it is read out.
7. Each PCR value in the transport packet transmitted by the MUX is modified in a PCR restamper 50 using the following equation:

$$\text{PCR}_{\text{new}} = \text{PCR}_{\text{old}} + (\text{Clock}_{\text{current}} - \text{Clock}_{\text{tagged}}) - \text{Delay}_{\text{max}} \quad (1)$$

where,

50 PCR_{new} : New PCR value after restamping;

PCR_{old} : Old PCR value before restamping;

$\text{Clock}_{\text{current}}$: Current Clock value at output time restamping 50;

$\text{Clock}_{\text{tagged}}$: Clock value tagged 41 at reception of the packet;

55 $\text{Delay}_{\text{max}}$: Maximum delay through the remux operation, which is a constant value to ensure that each PCR value never increases.

[0033] The scheduling scheme is an important feature of this invention. The main purpose of scheduling is to ensure that the transport buffers in the decoders never overflow. Scheduling of packets takes a lot of work if it is done in a brute force manner because it involves keeping track of the transport buffer fullness of every elementary stream in parallel. This may be too complicated for consumer applications apparatus, hence, we created a simpler way to do the job. This is based upon the following:

1. We can derive the transport buffer (85-88 in Fig. 5) emptying or leak rate (read-out rate) of each elementary stream from the information carried in the input transport stream. For example, the transport buffer leak rate is 54 Mbps for the Grand Alliance HD Video standard, 18 Mbps for SD Video and 2 Mbps for Audio, etc. We can know that if the remux rate is less than the transport buffer leak rate, no transport buffer monitoring is necessary because the transport buffer never overflows. Hence, we can reduce the number of transport buffers that we need to monitor. Transport buffer monitoring can be further simplified using the following approaches:

1. The buffer fullness of each transport buffer increases monotonically from the beginning of each received packet through the end of the packet; hence, we need to check the buffer fullness only at the end of each packet.
2. Remux knows its own output transport rate (the remux rate) as well as the transport buffer leak rates for each elementary stream; hence, it can know how much the buffer fullness of a transport buffer will change by sending a packet and by not sending a packet to the transport buffer, using the following equations:

$$20 \quad \text{Leak} = R_{\text{leak}} \cdot T_{\text{packet}} \quad (2)$$

$$25 \quad \Delta = S_{\text{packet}} - \text{Leak} \quad (3)$$

where,

Leak : Change of transport buffer fullness per one-packet period when transport buffer receives no packet;

R_{leak} : transport buffer leak rate;

T_{packet} : Packet period, i.e., $S_{\text{packet}}/R_{\text{remux}}$;

S_{packet} : Packet size;

R_{remux} : Remux rate;

Δ : Change of transport buffer fullness per one-packet period when transport buffer receives one packet.

35 Table 1

below shows an example of a parameters table that can be used for transport buffer monitoring.

	Rate _{leak}	Leak bits (bytes)	Delta bits (bytes)
SD	18 Mbps	1,082 (135)	422 (53)
Audio	1 Mbps	61 (8)	1,443 (180)
Others $\geq R_{\text{remux}}$	Mbps	NA or 0	NA or 0

($S_{\text{packet}} = 188$ bytes, $R_{\text{remux}} = 25$ Mbps)

This table can be expanded if there are other data types which have known transport buffer leak rates and require transport buffer monitoring.

3. Using the above results, the buffer fullness of each transport buffer can be calculated using the following equations:

$$50 \quad B_{\text{prev}} = B_{\text{last}}(i) - \text{Leak}(i)(C_{\text{current}} - C_{\text{last}}(i) - 1) \quad (4)$$

where,

55 i : Index of the elementary stream which the current packet belongs to;

B_{prev} : transport buffer fullness of the i-th elementary stream just before receiving the current packet;

$B_{\text{last}}(i)$: transport buffer fullness of the i-th elementary stream when the transport buffer has just finished receiving the last packet;

$L_{\text{Leak}}(i)$: transport buffer leak rate of the i -th elementary stream per one packet period;
 C_{current} : Value of output packet counter for the current packet;
 $C_{\text{last}}(i)$: Value of output packet counter for the last packet of the i -th elementary stream

If $B_{\text{prev}} < 0$, then $B_{\text{prev}} = 0$ (5)

$$B_{current} = B_{prev} + \text{Delta}(i) \quad (6)$$

where,

$B_{current}$: transport buffer fullness of the i -th elementary stream when the transport buffer has just finished receiving the current packet;

Delta(i): Change of transport buffer fullness of the i-th elementary stream by receiving one packet.

[0034] The scheduling algorithm goes as follows in the preferred embodiment:

Step 1. At every interval T_{packet} , check if a whole packet is in the packet store 44. If there is a packet (the current packet), go to Step 2, otherwise, go to Step 5.

Step 2. If the transport buffer leak rate of the i -th elementary stream, to which the current packet belongs, is obtained for the first time, calculate $\text{Leak}(i)$ and $\Delta(i)$, using equation (2) and equation (3), respectively, and initialize the parameters for transport buffer monitoring as follows, and go to Step 3:

$$B_{last}(i) = 0 \quad (7)$$

$$G_{\text{last}}(i) = G_{\text{current}} \quad (8)$$

Step 3. Calculate $B_{current}$ corresponding to the current packet, using equation (4), equation (5) and equation (6), and go to Step 4.

Step 4. If $B_{current}$ is equal to or less than the transport buffer size, output the current packet and update the parameters as follows and go to Step 6. Otherwise, go to Step 5:

$$B_{\perp,0}(i) = B_{\perp,0} \quad , \quad (9)$$

$$C_{\mu_1, \dots, \mu_k}(0) = C_{\mu_1, \dots, \mu_k} \quad (10)$$

Step 5. Output a Null packet and go to Step 6.

Step 6. Increment the output packet counter as follows and go to Step 1:

$$C_{current} = C_{current} + 1 \quad (11)$$

[0035] This simple scheduling scheme requires only a series of simple calculations per each packet period regardless of the number of the elementary streams in the transport stream and yet can prevent transport buffer overflow from happening. Moreover, as will be appreciated, where the system of Fig. 6 is employed as the packet processor of Fig. 5, it is capable of monitoring the buffer fullness in each of the target decoders 85 provided while delivering to them valid MPEG streams. This is because the above-noted algorithm can be executed each time a packet arrives at the store 44, because the scheduler 45 knows which data stream the packet belongs to, is keeping track of the packets of each separate data stream, and knows the leak rates of the respective transport buffers in the respective target decoders 85. Thus, in this particular system, a single scheduler is able to monitor multiple decoders.

[0036] If we combine the remuxing scheme explained above in connection with Fig. 3 with the overall recording proposal described in the referenced copending application, and remove redundancy, we can obtain a total DVCR solution, which is illustrated in Fig. 7. The same reference numerals as are used in Figs. 1 and 6 represent the same

components in Fig. 7. The new components which function similarly to those previously described include a packet sequencer tagger 60 corresponding to the tagger 10, a second local buffer B 61 corresponding to local buffer 15, and a MUX 62 which muxes tagged and restamped transport packets with the trickmode and null packets in the recording part prior to recording on the media. Whereas the previous Null packets meant MPEG Null packets to create a valid MPEG stream, these null packets 49 are used merely to fill gaps in the recording stream and serve no MPEG function. In the playback part, there is provided a filter 65 that serves to strip off undesired fill packets and the resultant transport stream is stored in a local buffer C 66 corresponding to local buffer 22, a decoding packet store 67 and decoding scheduler 68 which performs the reverse functions of the scheduler 45 in the encoding part, and a MUX 69 corresponding to block 17.

10 [0037] Recording then goes as follows:

1. The necessary packets are selected by the filter 40.
2. The packets containing PCR are tagged 41 with the local clock 39.
3. Each packet is stored and kept in local buffer A 42 until it is read out.
4. Whenever the packet store 44 is empty and there is at least one packet in buffer A 42, the first packet in buffer A 42 is read out and moved to the packet store 44. The necessary information of the packet is sent to the scheduler 45 at the same time.
5. The scheduler 45 checks whether outputting the packet in the packet store 44 will overflow the target transport buffer of the corresponding elementary stream or target decoder, as described above, and signals it to the MUX 62.
6. If the packet store 44 has a packet and the scheduler 45 signals that the target transport buffer will be OK, the packet in the packet store 44 is read out. The packet in the packet store 44 remains there until it is read out.
7. Each packet containing PCR is restamped 50 its PCR value using equation (1).
8. Each packet is tagged 60 with its packet sequence No., which may have discontinuity due to the scheduling.
9. Each packet is stored and kept in the local buffer B 61 until it is read out.
10. The packets read out from the buffer B 61 are multiplexed 62 with trickmode packets 16 and, if necessary, null packets 49, according to a trickmode recording scheme.

[0038] Playback goes as follows:

1. The necessary packets are selected by the filter 65.
2. Each packet is stored and kept in the local buffer C 66 until it is read out.
3. Whenever the packet store 67 is empty, a packet is read out from the buffer C 66 and moved to the packet store 67. The Packet Sequence No. tag of each packet is sent to the scheduler 68.
4. The scheduler 68 checks whether the Packet Sequence No. matches an internal packet counter (not shown or incorporated in the scheduler) and signals OK if both match.
5. If the scheduler 68 signals OK, the MUX 69 selects and reads out the packet in the packet store 67. Otherwise, the MUX 69 selects and sends out a Null packet. Every packet is sent out at the remux rate. Fig. 7 thus combines the remux scheme of Fig. 6 with the necessary components to allow recording and playback from a DVCR.

40 [0039] Figs. 9A and 9B show the improvement obtained under the same conditions described above in connection with Figs. 8A and 8B, respectively. In essence, shown in Fig. 9A, the rescheduler has delayed blocks 7 and 10, etc. (see arrows 75 and 76) long enough to prevent target buffer overflow. Similarly, blocks 6 and 9, etc. have been delayed at arrows 77 and 78 as shown in Fig. 9B to prevent overflow and loss of information.

[0040] In this way, a DVCR can reconstruct at playback, without loss of information, a transport stream that has the rate and timing exactly as scheduled by the remux at recording. With this scheme, the remux rate can be equal to or higher or lower than the recording rate as long as the net transport stream rate at the remux is equal to or less than the recording rate. Note that in the Fig. 6 embodiment, Null packets 49 are added. If substituted for the remuxer 80 in the Fig. 3 embodiment, Null packets would have to be deleted 82. This superfluous addition and deletion of MPEG Null packets is avoided in the Fig. 7 embodiment.

[0041] In the block diagrams of the figures, only the data flow is shown by the arrows. Those skilled in the art will understand that several of the blocks are interconnected for command and control signals that are not shown in the figure.

[0042] As previously indicated, the invention is also applicable to other data formats and other ways of preserving the critical timing data. It will also be appreciated that the circuitry and hardware to implement the various blocks shown, including software to the extent needed, will be evident to those skilled in the art not only from the detailed information supplied in the referenced related applications, but also from the below listed references:

(1) European patent application no. 492,704 (PHN 13.546)

(2) European patent application no. 595,411 (PHN 14.241)
 (3) European patent application no. 608,946 (PHN 14.449)
 (4) Grand Alliance HDTV System Specification, Draft document, February 22, 1994.
 (5) US patent specification no. 5,142,421 (PHN 13.537)

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[0043] While the invention has been described in connection with preferred embodiments, it will be understood that modifications thereof within the principles outlined above will be evident to those skilled in the art and thus the invention is not limited to the preferred embodiments but is intended to encompass such modifications.

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Claims

1. A method of processing a first transport stream of transport packets having a program clock reference (PCR) comprising plural bits representing valid timing-critical-information, and of unknown transport rate that may be of varying rate or bursty, to form a second transport stream of transport packets of known constant rate and of valid timing-critical-information, comprising the steps:

- (i) providing a local clock,
- (ii) processing the transport packets of the first transport stream in a serial manner while sampling the local clock when a bit of the PCR of each packet is processed and storing the sampled clock time for each packet,
- (iii) delaying the transport packets to avoid overflowing downstream buffers,
- (iv) when ready to deliver the transport packets without overflowing the downstream buffer, re-sampling the local clock at the time of the corresponding PCR bit and updating the PCR with the new sample time,
- (v) delivering the transport packets with updated PCRs to form the second transport stream.

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2. The method of claim 1, wherein the transport rate of the first transport stream is of the order of 50 Mbps and the transport rate of the second transport stream is of the order of 25 Mbps.

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3. A method of transmitting timing-critical data including a program clock reference (PCR) via an asynchronous channel downstream to apparatus including a target buffer having a limited read-out rate, comprising the steps:

- (i) receiving the timing-critical data subdivided into a stream of successive transport packets,
- (ii) determining the arrival time of each of the transport packets,
- (iii) temporarily storing the transport packets,
- (iv) computing the times when individual transport packets can be transmitted downstream to avoid overflowing the target buffer,
- (v) computing the departure time of each of said transport packets and modifying the PCR accordingly,
- (vi) transmitting downstream the transport packets in accordance with the computations of step (iv).

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4. A method as claimed in claim 3, further comprising re-stamping the transmitted transport packets, before transmission, with a new PCR.

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5. A method as claimed in claim 3, further comprising:

- (1) selecting the desired packets from the incoming transport stream by a filter,
- (2) storing for the selected packets the sampled arrival time of a local clock,
- (3) storing the selected transport packets via a local buffer in a packet store,
- (4) whenever the packet store is empty and there is at least one packet in the local buffer, reading out the first packet in the local buffer and moving same to the packet store while simultaneously passing on to a scheduler information concerning the packet,
- (5) computing in the scheduler whether outputting of the packet in the packet store will overflow the downstream target buffer and signalling it to a MUX,
- (6) if the packet store has a packet and the scheduler signals that the target buffer will be OK, selecting in the MUX and reading out the transport packet in the packet store; otherwise, selecting in the MUX and outputting a Null packet from a Null packet generator,
- (7) modifying the PCR in the transport packet transmitted by the MUX in a PCR re-stamper using the following equation:

$$\text{PCR}_{\text{new}} = \text{PCR}_{\text{old}} + (\text{Clock}_{\text{current}} - \text{Clock}_{\text{tagged}}) - \text{Delay}_{\text{max}} \quad (1)$$

5 where,

PCR_{new} : New PCR value after restamping;
PCR_{old} : Old PCR value before restamping;
Clock_{current} : Current Clock value at restamping;
Clock_{tagged} : Clock value tagged at reception of the packet;
10 Delay_{max} : Maximum delay through restamping, which is a constant value to ensure that each PCR value never increases.

15 6. A method as claimed in claim 5, wherein said scheduler operates by knowing the read-out rate of the target buffer, by computing the downstream target buffer fullness at an output transport rate, and by delaying the transmission of any transport packets if the computation indicates that the target buffer will overflow.

7. A method as claimed in claim 5, further comprising means to record the selected packets, further comprising the steps:

20 (viii) tagging the transport packets having the modified PCR with a sequence of arrival (SOA) tag,
(ix) transmitting the tagged packets to a recorder.

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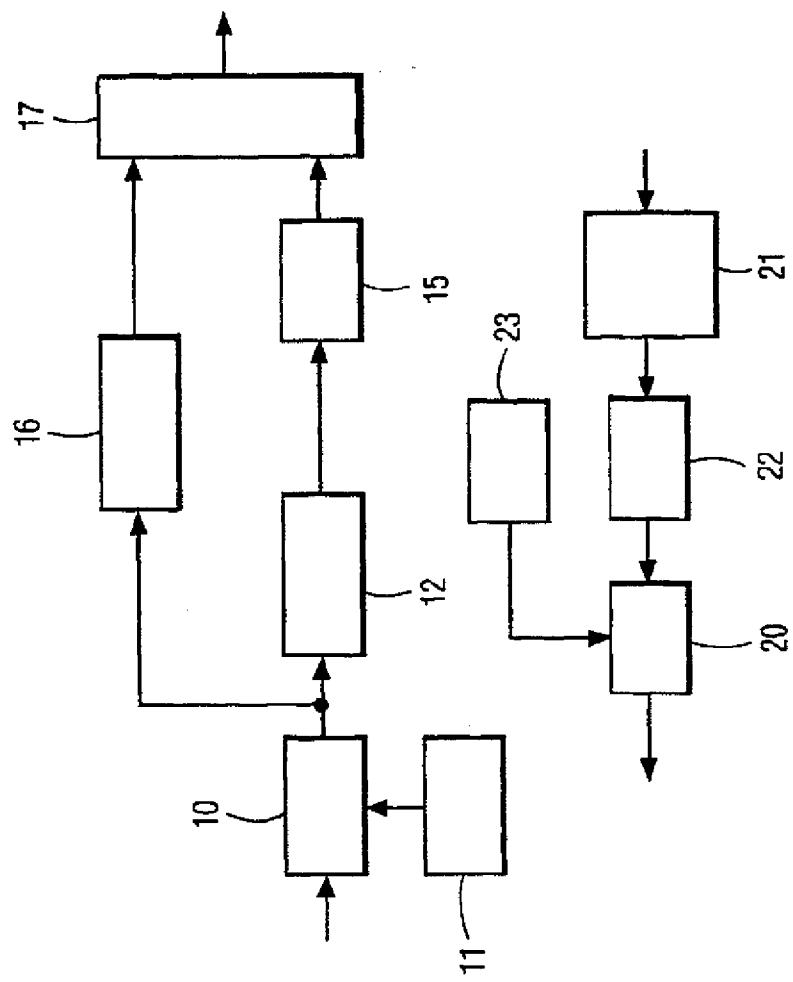
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FIG. 1



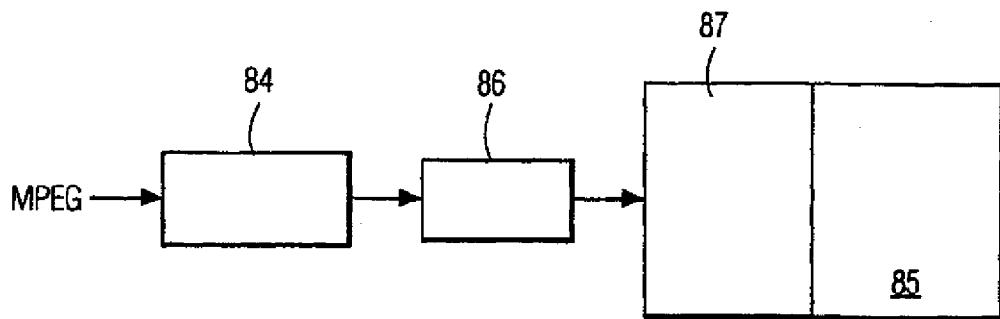
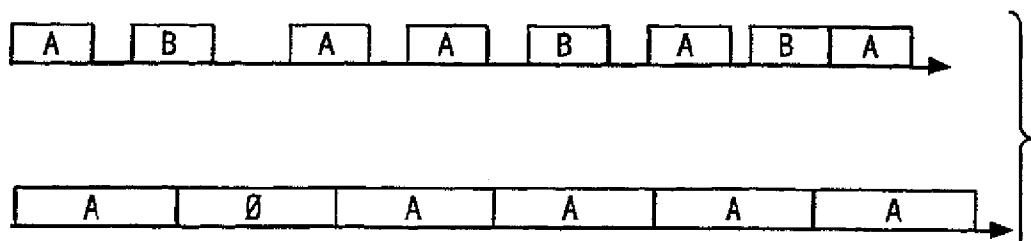
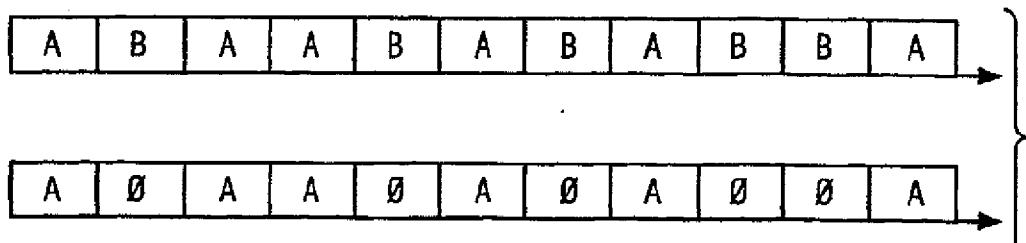
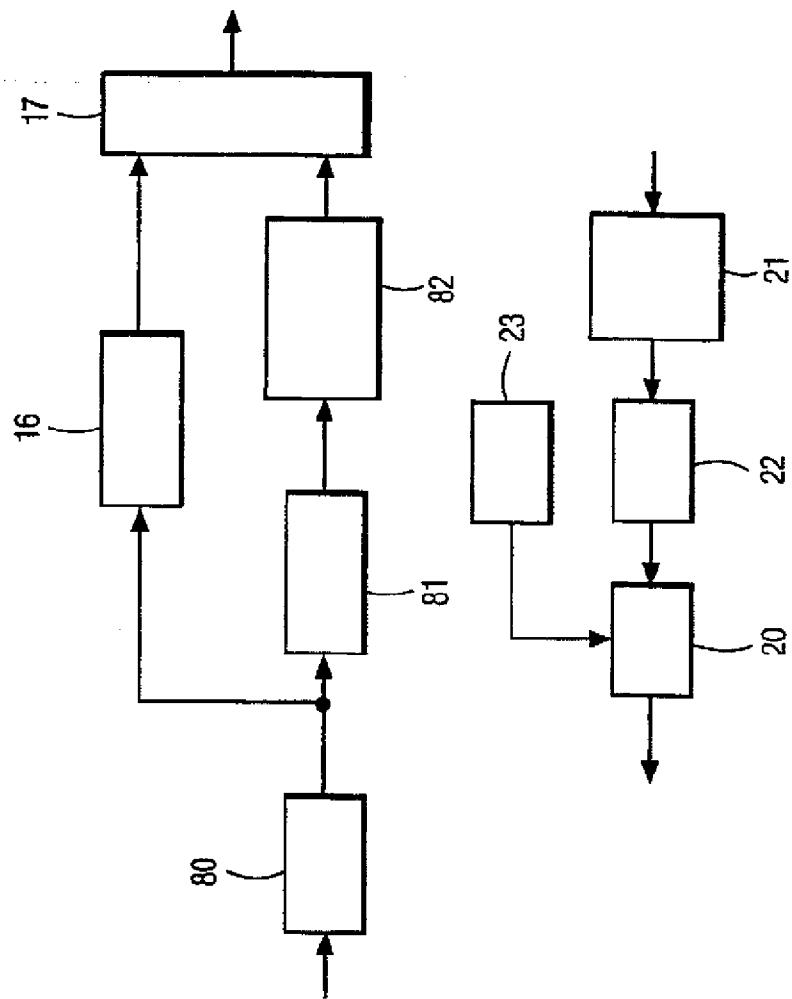


FIG. 3



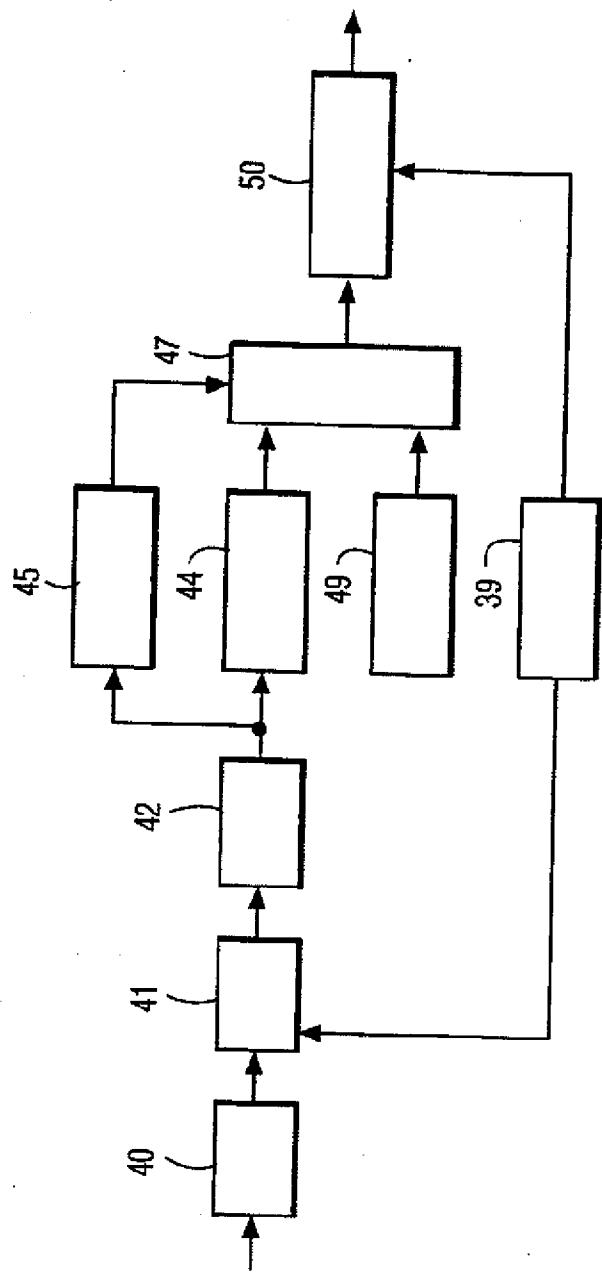
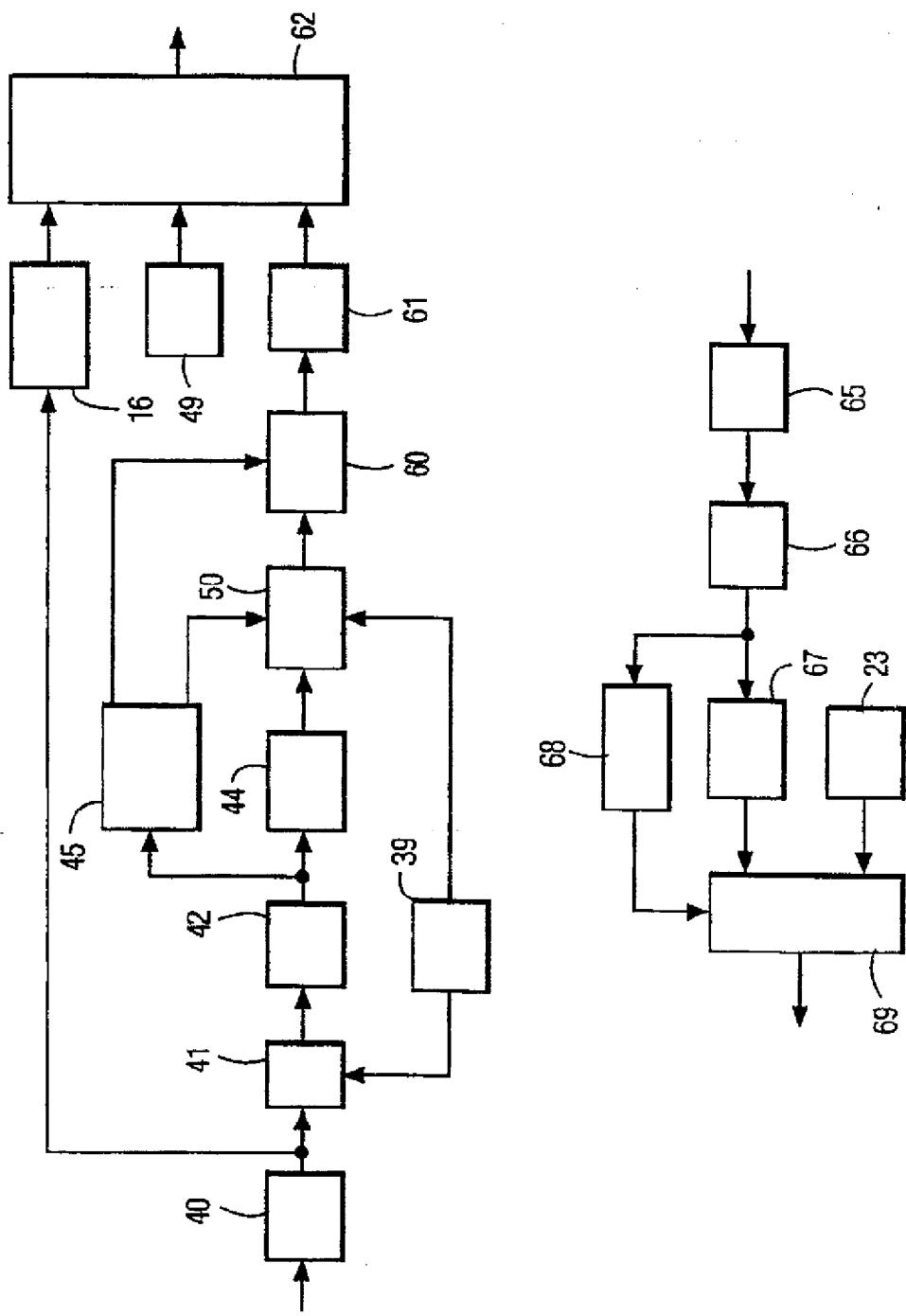


FIG. 6

FIG. 7



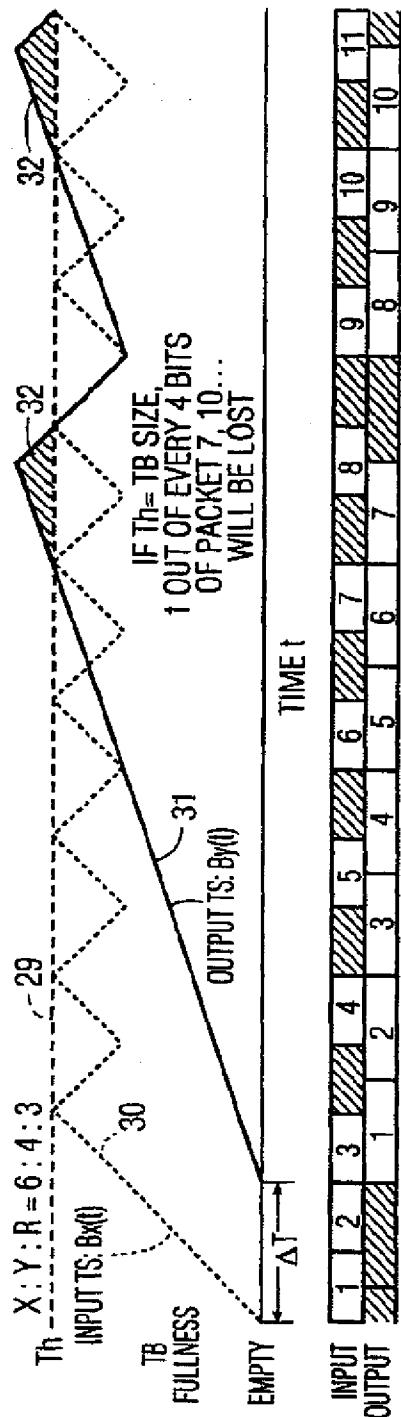


FIG. 8A

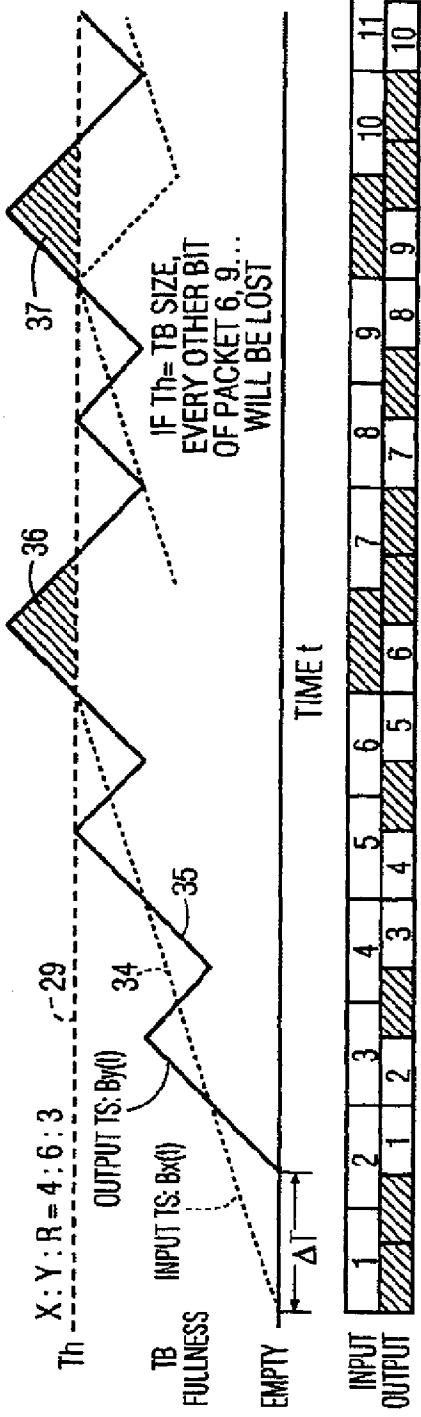


FIG. 8B

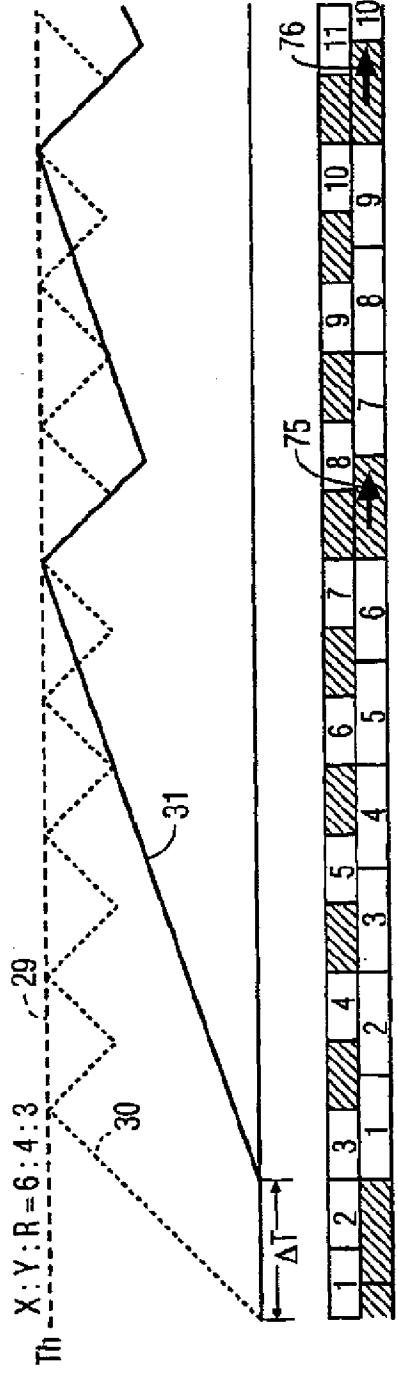


FIG. 9A

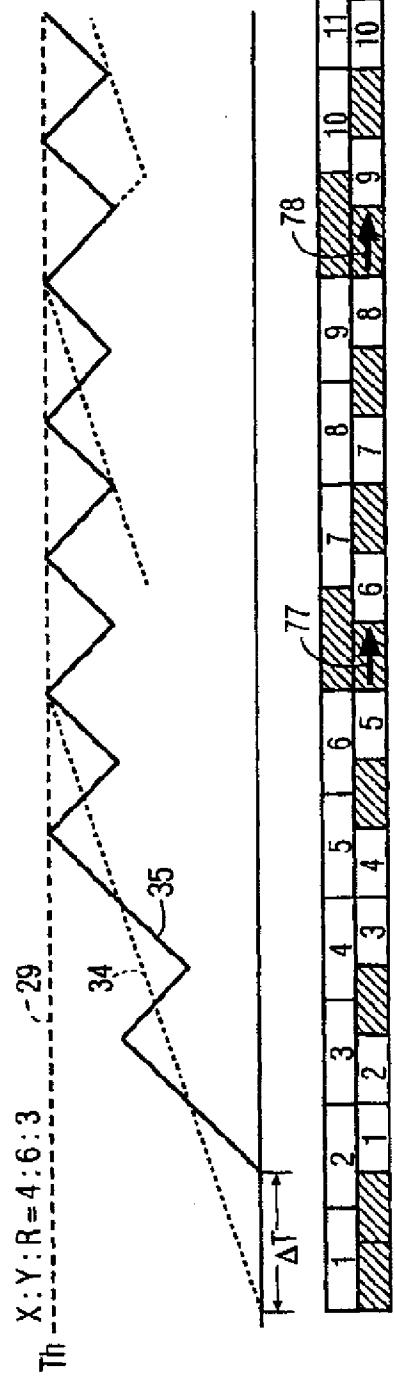
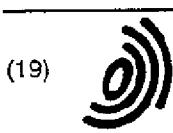


FIG. 9B



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(11)

EP 0 921 690 A3

(12)

EUROPEAN PATENT APPLICATION

(88) Date of publication A3:
14.07.1999 Bulletin 1999/28

(51) Int.Cl.6: H04N 7/62

(43) Date of publication A2:
09.06.1999 Bulletin 1999/23

(21) Application number: 99200377.2

(22) Date of filing: 30.08.1995

(84) Designated Contracting States:
AT DE FR GB

Designated Extension States:
LT LV SI

(30) Priority: 07.09.1994 US 302144

(62) Document number(s) of the earlier application(s) in
accordance with Art. 76 EPC:
95927940.7 / 0 727 126

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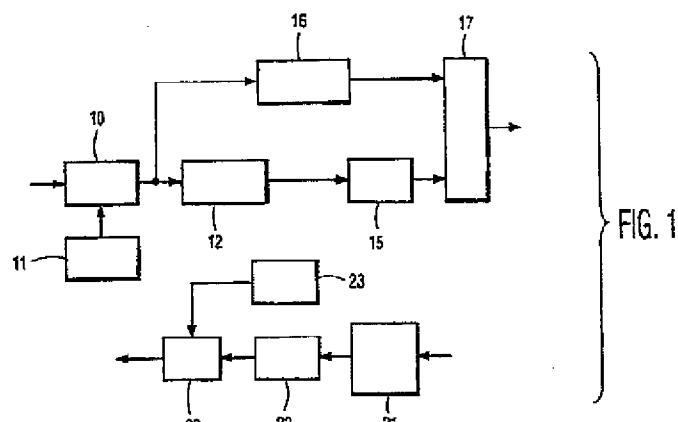
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(54) **MPEG information signal conversion system**

(57) A method of transmitting timing critical data via an asynchronous channel. The timing critical data can be an MPEG transport stream of packets. The asynchronous channel can be a computer or telephone network, a digital storage media such as a digital VCR, or a digital interface. The packets are processed serially through a remuxer to obtain a constant rate and delivered to and consumed by one or more target decoders, for example, inside a TV set or in a set-top decoder. To prevent overflow of the transport buffers inside these de-

coders, a single monitor-scheduler is provided which monitors the transport buffers and delivers to each the packets wanted scheduled so as to avoid buffer overflow and loss of information. The method also includes restamping the transport packets with new PCRs. The remuxing scheme is simple enough to implement on DVCR or other consumer applications. Also described is a method for recording the output stream which selects out desired program material and tags the transport packets with SOA tags.



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EUROPEAN SEARCH REPORT

Application Number
EP 99 20 0377

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	US 5 287 182 A (HASSELL BARIN G ET AL) 15 February 1994 * abstract * * column 1, line 24 - line 50 * * column 2, line 5 - line 13 * * column 4, line 7 - column 6, line 14 * —	1-5	H04N7/62
A	US 5 260 978 A (FLEISCHER PAUL E ET AL) 9 November 1993 * abstract * * column 3, line 48 - column 4, line 55 * * claims 1,2 * —	1-5	
A	GB 2 274 041 A (BRITISH BROADCASTING CORP) 6 July 1994 * page 2, line 3 - page 4, line 2 * —	1-7	
A	EP 0 515 101 A (AMERICAN TELEPHONE & TELEGRAPH) 25 November 1992 * page 11, line 1 - page 13, line 50 * —	1-7	
TECHNICAL FIELDS SEARCHED (Int.Cl.6)			
H04N			
The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	25 May 1999	Marie-Julie, J-M	
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EPO FORM 1503 03/92 (P40C01)			

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EP 99 20 0377

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26-05-1999

Patent document cited in search report		Publication date		Patent family member(s)	Publication date
US 5287182	A	15-02-1994		CA 2097349 A,C EP 0577329 A JP 6097927 A	03-01-1994 05-01-1994 08-04-1994
US 5260978	A	09-11-1993		NONE	
GB 2274041	A	06-07-1994		GB 2242097 A,B	18-09-1991
EP 0515101	A	25-11-1992		US 5159447 A DE 69221028 D DE 69221028 T JP 2648418 B JP 6253277 A KR 9505618 B	27-10-1992 04-09-1997 13-11-1997 27-08-1997 09-09-1994 27-05-1995

EPO FORM P0495
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